**DAILY ASSESSMENT FORMAT**

|  |  |  |  |
| --- | --- | --- | --- |
| **Date:** | **29-05-2020** | **Name:** | **Pragati M Kundalkar** |
| **Course:** | **Logic design** | **USN:** | **4AL17EC072** |
| **Topic:** | **Analysis of clocked sequential circuits, digital clock design** | **Semester & Section:** | **6TH SEM**  **B - SEC** |
| **Github Repository:** | **Pragati-m-k** |  |  |

|  |
| --- |
| **FORENOON SESSION DETAILS** |
| C:\Users\User\AppData\Local\Microsoft\Windows\INetCache\Content.Word\logic_2_1.jpg  C:\Users\User\AppData\Local\Microsoft\Windows\INetCache\Content.Word\logic_2_2.jpg |

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Date:** | **29-05-2020** | **Name:** | **Pragati M Kundalkar** | |
| **Course:** | **Python programming** | **USN:** | **4AL17EC072** | |
| **Topic:** | **Object oriented programming** | **Semester & Section:** | **6TH SEM**  **B - SEC** | |
| **AFTERNOON SESSION DETAILS** | | | |
| C:\Users\User\AppData\Local\Microsoft\Windows\INetCache\Content.Word\python_10_1.jpg  C:\Users\User\AppData\Local\Microsoft\Windows\INetCache\Content.Word\python_10_2.jpg | | | |